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In the Claims:

Please amend the Claims as follows, Cancelling Claims 1 and 2, and Amending Claims 3, 10, 11, 12, 19 and 20:

– 1 - 2. (Cancelled)

1 3. (Currently amended) A photosensitive array comprised of a plurality of pixels arranged
2 in columns and rows, wherein the pixels are configured into a multiplicity of groups of at
3 least a first pixel and a second pixel, each ~~said~~ group including

4 a shared pixel output transistor having a sense electrode and an output electrode;
5 and

6 a shared reset transistor having a gate coupled to receive a reset signal and an
7 output coupled to the sense electrode of the associated shared pixel output transistor; and

8 ~~each of~~ said first and second pixels each including a photosensitive element having
9 an output electrode, with both said output electrodes being coupled together to the sense
10 electrode of the shared pixel output transistor and a respective gate electrode coupled to
11 receive respective first and second pixel gating signals.

1 4. (Original) A photosensitive array according to Claim 3 wherein said first and second
2 pixels of each said group are both disposed in the same column.

1 5. (Original) A photosensitive array according to Claim 3 wherein the first and second
2 pixels of each said group are disposed in successive columns in a single row.

1 6. (Original) A photosensitive array according to Claim 3 wherein the photosensitive
2 element of each said pixel includes a photogate that captures and accumulates photon
3 generated charge; a sense gate positioned on said photogate; and a sense node that

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4 surrounds the photogate.

1 7. (Original) A photosensitive array according to Claim 6 wherein the photosensitive
2 elements of said first and second pixels are adjacent one another and the sense nodes
3 thereof share a segment in common with one another.

1 8. (Original) A photosensitive array according to Claim 3 wherein each said
2 photosensitive element includes a sense node FET having a gate electrode.

1 9. (Original) A photosensitive array according to Claim 3 wherein each said
2 photosensitive element is formed of a charge snare device.

1 10. (Currently amended) A photosensitive array according to Claim 3 wherein each said
2 photosensitive element is formed of a photogate and a transfer gate transistor, wherein
3 one electrode of [a] the transfer gate transistor thereof is connected to a photodiode and
4 another electrode thereof is connected to said sense electrode, and a gate of said transfer
5 gate transistor is connected to receive a control signal to operate timing of transfer and
6 reset of said photodiode.

1 11. (Currently amended) A photosensitive array according to Claim 3 wherein each said
2 photosensitive element is formed of a photodiode and a transfer gate transistor.

1 12. (Currently amended) A photosensitive array according to Claim 11 wherein one
2 electrode ~~transfer transistor~~ of said transfer gate transistor is connected to a photodiode
3 and another electrode is connected to said sense electrode, and a gate of said transfer gate
4 transistor is connected to receive a control signal to operate timing of transfer and reset of

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5 said photodiode.

1 13. (Original) A photosensitive array according to Claim 3 further comprising color
2 filters on said first and second photosensitive elements.

1 14. (Original) A photosensitive array according to Claim 3 wherein said groups each
2 include a third pixel and a fourth pixel, each of which includes a photosensitive element
3 having an output electrode coupled to the sense electrode of said shared pixel output
4 transistor, and a gate electrode to receive a respective gating signal.

1 15. (Original) A photosensitive array according to Claim 14 wherein said groups each
2 include a second reset transistor.

1 16. (Original) A photosensitive array according to Claim 3 wherein said groups each
2 include a third and fourth pixel, each of which includes a photosensitive element having
3 an output electrode, and a gate electrode to receive a respective gating signal; a second
4 pixel output transistor having a sense electrode coupled to the output electrodes of the
5 third and fourth pixel photosensitive elements; and a second reset transistor having a gate
6 coupled to receive a second reset signal and an output coupled to the sense electrode of
7 said second pixel output transistor.

1 17. (Original) A photosensitive array according to Claim 3 wherein said reset transistor
2 is an FET having a drain thereof connected to the sense electrode of said pixel output
3 transistor.

1 18. (Original) A photosensitive array according to Claim 17 wherein said pixel output

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2 transistor includes an FET having its gate electrode connected to the drain of the FET of
3 said reset transistor.

1 19. (Currently amended) A photosensitive array according to Claim 3 wherein said group
2 includes at least a third pixel with the third pixel also ~~sharing said~~ having an output
3 electrode coupled to the sense electrode of the shared pixel output transistor.

1 20. (Original) A photosensitive array according to Claim 3, wherein said group includes
2 first, second, third, and fourth pixels, in a 2X2 arrangement, all ~~sharing said~~ said pixels
3 having associated output electrodes coupled together to the sense electrode of the shared
4 pixel output transistor. —